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**Application Number:** 

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DESIGN OF HIGH-SPEED APPROXIMATE REDUNDANT BINARY MULTIPLIER USING 4:2, 5:2 & 7:2 COMPRESS Sunkara Yuha Sridevi Address: M-Tech 2nd year student in the ECE Department of G. Narayanamma Institute of Science (For Women) Hyderabad, Telangana, India-500104.

Applicant Type Address Sr.No. Applicant Name G. Narayanamma Institute of Technology & Science (For Women) Hyderabad 500104 G. Narayanamma Institute of NP Technology.

Sr.No.	Inventor Name Sunkara Yuha Sridevi	Country	India	Address				
1				M-Tech 2nd year student in the ECE Department of G. Narayanamma Institute of Technology Women) Hyderabad, Telangana, India-500104. sridevisunkara123@gmail.com				
2	K. Ragini	India	India	Professor, ECE Department of G. Narayanamma Institute of Technology & Science (For Worn Telangana, India-500104, ragini,kanchimi2011@gmail.com				
3	Swapna Raghunath	India		Associate Professor, ECE Department of G. Narayanamma Institute of Technology & Science Hyderabad, Telangana, India-500104, swapna.karnam1@grnail.com				

## Add Additional Inventor(if any)

APP.No.	NAME	ADDRESS	LOCALITY	COUNTRY	STATE	CITY	NATIONA
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