

**DEPARTMENT OF ELECTRONICS & COMMUNICATION
ENGINEERING**

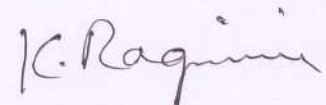
CERTIFICATE

This is to certify that the dissertation entitled "**REVERSIBLE RANDOM ACCESS MEMORY WITH IMPROVED QUANTUM COST AND DELAY**", being submitted by Ms. **Bhavana Majji**, having HT No.: **20251DB103**, in partial fulfillment of the requirements for the Award of the Degree of Master of Technology in **ELECTRONICS AND COMMUNICATION ENGINEERING**, with **DIGITAL ELECTRONICS AND COMMUNICATION ENGINEERING** Specialization, is a record of bonafide work carried out by her.

The results embodied in this Dissertation Work have not been submitted to any other University or Institute for the Award of any other Degree or Diploma.

Date:

7/10/21



INTERNAL GUIDE

Dr. K. RAGINI

PROFESSOR,

ECE DEPT.