

**DEPARTMENT OF ELECTRONICS & COMMUNICATION  
ENGINEERING**

**CERTIFICATE**

This is to certify that the dissertation entitled "SYNTHESIS OF 16 BIT CARRY SELECT ADDER USING BINARY TO EXCESS-1 CONVERTER", being submitted by Ms.K.Vaishnavi, having HT No.: 20251DB105, in partial fulfillment of the requirements for the Award of the Degree of Master of Technology in **ELECTRONICS AND COMMUNICATION ENGINEERING**, with **DIGITAL ELECTRONICS AND COMMUNICATION ENGINEERING** Specialization, is a record of bonafide work carried out by her, under my guidance and supervision. The results of investigations enclosed in this Report have been verified and found to be satisfactory.

The results embodied in this Dissertation Work have not been submitted to any other University or Institute for the Award of any other Degree or Diploma.

*V.Radha Krishna* → 8/10/21

INTERNAL GUIDE  
V.RADHA KRISHNA  
Asst. PROFESSOR,  
ECE DEPT.

Date: 8/10/21