

**DEPT. OF ELECTRONICS & COMMUNICATION ENGG
CERTIFICATE**

THIS IS TO CERTIFY THAT THE PROJECT ENTITLED

**DESIGN OF FLIPFLOPS FOR HIGH PERFORMANCE
VLSI APPLICATIONS USING DEEP SUBMICRON CMOS
TECHNOLOGY**

IS THE BONAFIDE WORK OF

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AWARD OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND
COMMUNICATION ENGINEERING DURING THE YEAR 2020-2021.


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