

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD – 500 085

EXAMINATION BRANCH

II YEAR B.TECH –II SEMESTER – R16 REGULATION –SUPPLEMENTARY EXAMINATIONS AUGUST/SEPTEMBER-2022

TIME TABLE

TIME → FN: 9:45 AM TO 12:45 PM

BRANCH	DATE, SESSION AND DAY					
	25-08-2022 THURSDAY	29-08-2022 MONDAY	01-09-2022 THURSDAY	03-09-2022 SATURDAY	05-09-2022 MONDAY	07-09-2022 WEDNESDAY
ELECTRONICS & COMMUNICATION S ENGINEERING (04- ECE)	Business Economic and Financial Analysis	-	Control Systems	Analog Communications	Pulse and Digital Circuits	Switching Theory & Logic Design
ELECTRICAL AND ELECTRONICS ENGINEERING (02- EEE)	Business Economic and Financial Analysis	---	Control Systems	Power Systems - I	Electrical Machines-II	Switching Theory & Logic Design
COMPUTER SCIENCE & ENGINEERING (05- CSE)	Business Economic and Financial Analysis	Computer Organization	Database Management Systems	Operating Systems	Formal Languages and Automata Theory	--

DATE: 16-08-2022

Sd/-
CONTROLLER OF EXAMINATIONS

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD – 500 085

EXAMINATION BRANCH

II YEAR B.TECH-II SEMESTER– R16 REGULATION SUPPLEMENTARY EXAMINATIONS AUGUST/SEPTEMBER-2022

TIMETABLE

TIME → FN: 9:45 AM TO 12:45 PM

BRANCH	DATE, SESSION AND DAY					
	25-08-2022 THURSDAY	29-08-2022 MONDAY	01-09-2022 THURSDAY	03-09-2022 SATURDAY	05-09-2022 MONDAY	07-09-2022 WEDNESDAY
INFORMATION TECHNOLOGY (12-IT)	Business Economic and Financial Analysis	Computer Organization	Database Management Systems	Operating Systems	Formal Languages and Automata Theory	---
ELECTRONICS AND TELEMATICS ENGINEERING (17-ETM)	Business Economic and Financial Analysis	--	Control Systems	Analog Communications	Pulse and Digital Circuits	Switching Theory & Logic Design

DATE: 16-08-2022

Sd/-
CONTROLLER OF EXAMINATIONS

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD – 500 085

EXAMINATION BRANCH

II YEAR B.TECH - I SEMESTER – R16 REGULATION SUPPLEMENTARY EXAMINATIONS AUGUST/SEPTEMBER-2022

T I M E T A B L E

TIME → FN: 9:45 AM TO 12:45 PM

BRANCH	DATE, SESSION AND DAY					
	30-08-2022 TUESDAY	02-09-2022 FRIDAY	06-09-2022 TUESDAY	08-09-2022 THURSDAY	10-09-2022 SATURDAY	12-09-2022 MONDAY
INFORMATION TECHNOLOGY (12-IT)	MATHEMATICS – IV	DATA STRUCTURES THROUGH C++	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN	OBJECT ORIENTED PROGRAMMING THROUGH JAVA	--
ELECTRICAL AND ELECTRONICS ENGINEERING (02- EEE)	MATHEMATICS – IV	NETWORK THEORY	ELECTRONIC CIRCUITS	ELECTROMAGNETIC FIELDS	ELECTRICAL MACHINES-I	--
ELECTRONICS AND TELEMATICS ENGINEERING (17-ETM)	MATHEMATICS – IV	ANALOG ELECTRONICS	NETWORK ANALYSIS	SIGNALS AND STOCHASTIC PROCESS	ELECTRICAL TECHNOLOGY	--

DATE: 16-08-2022

Sd/-
CONTROLLER OF EXAMINATIONS

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD – 500 085

EXAMINATION BRANCH

II YEAR B.TECH - I SEMESTER – R16 REGULATION SUPPLEMENTARY EXAMINATIONS AUGUST/SEPTEMBER -2022

T I M E T A B L E

TIME → FN: 9:45 AM TO 12:45 PM

BRANCH	DATE, SESSION AND DAY					
	30-08-2022 TUESDAY	02-09-2022 FRIDAY	06-09-2022 TUESDAY	08-09-2022 THURSDAY	10-09-2022 SATURDAY	12-09-2022 MONDAY
ELECTRONICS & COMMUNICATIONS ENGINEERING (04- ECE)	MATHEMATICS – IV	ANALOG ELECTRONICS	NETWORK ANALYSIS	SIGNALS AND STOCHASTIC PROCESS	ELECTRICAL TECHNOLOGY	—
COMPUTER SCIENCE & ENGINEERING (05- CSE)	MATHEMATICS – IV	DATA STRUCTURES THROUGH C++	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN	OBJECT ORIENTED PROGRAMMING THROUGH JAVA	—

DATE: 16-08-2022

Sd/-
CONTROLLER OF EXAMINATIONS

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY , HYDERABAD – 500 085

E X A M I N A T I O N B R A N C H

II YEAR B.TECH - II SEMESTER–R15,R13 REGULATION-SUPPLEMENTARY EXAMINATIONS AUGUST/SEPTEMBER-2022

T I M E T A B L E

TIME → FN: 9:45 AM TO 12:45 PM

BRANCH	DATE & DAY					
	25-08-2022 THURSDAY	29-08-2022 MONDAY	01-09-2022 THURSDAY	03-09-2022 SATURDAY	05-09-2022 MONDAY	07-09-2022 WEDNESDAY
ELECTRICAL AND ELECTRONICS ENGINEERING (02-E E E)	ELECTRONIC CIRCUITS	ELECTRICAL MACHINES-II	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	POWER SYSTEMS-I	SWITCHING THEORY AND LOGIC DESIGN	NETWORK THEORY
INFORMATION TECHNOLOGY (12- I T)	ENVIRONMENTAL STUDIES	PRINCIPLES OF PROGRAMMING LANGUAGES	DATA COMMUNICATION	JAVA PROGRAMMING	DESIGN AND ANALYSIS OF ALGORITHMS	DATABASE MANAGEMENT SYSTEMS
ELECTRONICS AND COMMUNICATIONS ENGINEERING (04-E C E)	ENVIRONMENTAL STUDIES	PRINCIPLES OF ELECTRICAL ENGINEERING	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	DIGITAL DESIGN USING VERILOG HDL	ELECTRONIC CIRCUIT ANALYSIS	PULSE AND DIGITAL CIRCUITS
COMPUTER SCIENCE AND ENGINEERING (05-C S E)	ENVIRONMENTAL STUDIES	DESIGN AND ANALYSIS OF ALGORITHMS	COMPUTER ORGANIZATION	JAVA PROGRAMMING	FORMAL LANGUAGES AND AUTOMATA THEORY	DATABASE MANAGEMENT SYSTEMS
ELECTRONICS AND TELEMATICS ENGINEERING (17-E T M)	ENVIRONMENTAL STUDIES	PRINCIPLES OF ELECTRICAL ENGINEERING	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	ANALOG COMMUNICATIONS	ELECTRONIC CIRCUIT ANALYSIS	PULSE AND DIGITAL CIRCUITS

DATE: 16-08-2022

Sd/-
CONTROLLER OF EXAMINATIONS

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD – 500 085

EXAMINATION BRANCH

II YEAR B.TECH - I SEMESTER – R15,R13 REGULATION SUPPLEMENTARY EXAMINATIONS AUGUST/SEPTEMBER-2022

T I M E T A B L E

TIME → FN:9:45 AM TO 12:45 PM

BRANCH	DATE, SESSION AND DAY					
	30-08-2022 TUESDAY	02-09-2022 FRIDAY	06-09-2022 TUESDAY	08-09-2022 THURSDAY	10-09-2022 SATURDAY	12-09-2022 MONDAY
COMPUTER SCIENCE & ENGINEERING (05- CSE)	PROBABILITY & STATISTICS	ELECTRONIC DEVICES & CIRCUITS	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN	DATA STRUCTURES	BASIC ELECTRICAL ENGINEERING
ELECTRICAL AND ELECTRONICS ENGINEERING (02- EEE)	ELECTRICAL CIRCUITS	ELECTRONIC DEVICES & CIRCUITS	FLUID MECHANICS AND HYDRAULIC MACHINERY	ELECTRO MAGNETIC FIELDS	ELECTRICAL MACHINES - I	MATHEMATICS – III
INFORMATION TECHNOLOGY (12-IT)	PROBABILITY & STATISTICS	ELECTRONIC DEVICES & CIRCUITS	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN AND COMPUTER ORGANIZATION	DATA STRUCTURES	BASIC ELECTRICAL ENGINEERING
ELECTRONICS & COMMUNICATIONS ENGINEERING (04- ECE)	ELECTRICAL CIRCUITS	ELECTRONIC DEVICES & CIRCUITS	SIGNALS & SYSTEMS	SWITCHING THEORY AND LOGIC DESIGN	PROBABILITY THEORY & STOCHASTIC PROCESSES	MATHEMATICS – III
ELECTRONICS AND TELEMATICS ENGINEERING (17-ETM)	ELECTRICAL CIRCUITS	ELECTRONIC DEVICES & CIRCUITS	SIGNALS & SYSTEMS	SWITCHING THEORY AND LOGIC DESIGN	PROBABILITY THEORY & STOCHASTIC PROCESSES	MATHEMATICS – III

DATE: 16-08-2022

Sd/-
CONTROLLER OF EXAMINATIONS