

<b>GNITS</b>	<b>GNITS-D / ECE / TTL / 041 / 00</b>
<b>YEAR WISE TIMETABLE</b>	<b>DEPARTMENT: ECE</b>

Branch: ECE Course: B.Tech Year: 3/4 A Semester: I Room No.: D201 w.e.f: 29-08-22

Time / Day	1 9:00 - 10:00	2 10:00 - 11:00	3 11:10-12:10	12:10 PM - 12:50 PM	4 12:50-1:50	5 1.50-2.50	6 2.50-3.50
Mon	VLSID	CO/ITC	FDS/JAVA/ IDA	L U N C H	DSP	DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING	
Tue	DSP Lab(A1)/ECAD Lab(A2)/ESS Lab(A3)				EMTL	VLSID	DSP
Wed	CO/ITC	EMTL	FDS/JAVA/ IDA		DSP Lab(A2)/ECAD Lab(A3)/ESS Lab(A1)		
Thu	DSP Lab(A3)/ECAD Lab(A1)/ESS Lab(A2)				DSP	EMTL	VLSID
Fri	DSP	EMTL	EXTRA CLASSES / TRAINING		VLSID	CO/ITC	FDS/JAVA/ IDA
Sat	DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING				DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING		

(\* ) = Tutorial 18 Theory hours + 9 Lab hours Note: 11:00 AM to 11:10 AM - Break

Subject	No. of Classes	Faculty	Lab Batches
VLSI Design (PC115CM)	4	V. Radhakrishna	A1 - 20251A0401 - 420, 21255A0401 - 402
EM Theory and Transmission Lines (PC115CD)	4	Dr. P.V.D. Somasekhar	A2 - 20251A0421 - 440, 21255A0403 - 404
Digital Signal Processing (PC115BY)	4	Prof. Ch. Ganapathy Reddy	A3 - 20251A0441 - 460, 21255A0405 - 406, 20881A0496
Computer Organization (PE115BS)	3	M. Shanthi (D305)	
Information Theory and Coding (PE115CF)	3	P. Lavanya (D201)	
Fundamentals of Data Structures (OE115KA)	3	Pooja V. Phad (D201)	
Java Programming (OE115KB)	3	B. Vijay Kumar (D302)	
Introduction to Data Analytics (OE115KF)	3	Dr. M. Madhavi Lata (F9 - IT Block)	
Digital Signal Processing Lab (PC11538)	3	K. Swathi (A1), A. Sarada (A2), P. Chandrasekhar (A3)	
ECAD & VLSI Lab (PC11539)	3	P. Roopa Ranjani (A1), G. Krishna Kishore (A2), V. Radhakrishna (A3)	
Employability and Soft Skills Lab (HS11542)	3	Dr. P. Aparna (A1), B. Himabala (A2), V. Jahnavi (A3)	

Dept. Timetable Coordinator

Main Timetable Coordinator

Head of the Department

Principal

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<b>YEAR WISE TIMETABLE</b>	<b>DEPARTMENT: ECE</b>

Branch: ECE Course: B.Tech Year: 3/4 B Semester: I Room No.: D302 w.e.f: 29-08-22

Time / Day	1 9:00 - 10:00	2 10:00 - 11:00	3 11:10-12:10	12:10 PM - 12:50 PM	4 12:50-1:50	5 1.50-2.50	6 2.50-3.50
<b>Mon</b>	DSP	CO/ITC	FDS/JAVA/ IDA	L U N C H	EMTL	DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING	
<b>Tue</b>	DSP Lab(B1)/ECAD Lab(B2)/ESS Lab(B3)				VLSID	DSP	EMTL*
<b>Wed</b>	CO/ITC	VLSID	FDS/JAVA/ IDA		DSP	EMTL	EXTRA CLASSES / TRAINING
<b>Thu</b>	VLSID	EMTL	DSP		DSP Lab(B2)/ECAD Lab(B3)/ESS Lab(B1)		
<b>Fri</b>	DSP Lab(B3)/ECAD Lab(B1)/ESS Lab(B2)				VLSID*	CO/ITC	FDS/JAVA/ IDA
<b>Sat</b>	DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING				DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING		

(\* ) = Tutorial 18 Theory hours + 9 Lab hours Note: 11:00 AM to 11:10 AM - Break

Subject	No. of Classes	Faculty	Lab Batches
VLSI Design (PC115CM)	4	Dr. K. Ragini	B1 - 20251A0461 - 480, 21255A0407 - 408
EM Theory and Transmission Lines (PC115CD)	4	Dr. Swapna Raghunath	B2 - 20251A0481 - 4A0, 21255A0409 - 410
Digital Signal Processing (PC115BY)	4	B. Tulasi Sowjanya	B3 - 20251A04A1 - 4C0, 21255A0411 - 412
Computer Organization (PE115BS)	3	M. Shanthi (D305)	
Information Theory and Coding (PE115CF)	3	P. Lavanya (D201) / A. Sujatha Reddy (D302)	
Fundamentals of Data Structures (OE115KA)	3	Pooja V. Phad (D201) / G. Nagababu (D305)	
Java Programming (OE115KB)	3	B. Vijay Kumar (D302)	
Introduction to Data Analytics (OE115KF)	3	Dr. M. Madhavi Lata (F9 - IT Block)	
Digital Signal Processing Lab (PC11538)	3	B. Tulasi Sowjanya (B1), K. Swathi (B2), A. Sarada (B3)	
ECAD & VLSI Lab (PC11539)	3	V. Radhakrishna (B1, B2), V. Shankar (B3)	
Employability and Soft Skills Lab (HS11542)	3	P.V. Asha Latha (B1), Dr. B. R. Lakshmi (B2, B3)	

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<b>YEAR WISE TIMETABLE</b>	<b>DEPARTMENT: ECE</b>

Branch: ECE Course: B.Tech Year: 3/4 C Semester: I Room No.: D305 w.e.f: 29-08-22

Time / Day	1 9:00 - 10:00	2 10:00 -11:00	3 11:10-12:10	12:10 PM – 12:50 PM	4 12:50-1:50	5 1.50-2.50	6 2.50-3.50
<b>Mon</b>	VLSID	CO/ITC	FDS/JAVA/ IDA	L U N C H	DSP Lab(C1)/ECAD Lab(C2)/ESS Lab(C3)		
<b>Tue</b>	EMTL	DSP	EXTRA CLASSES / TRAINING		DSP Lab(C2)/ECAD Lab(C3)/ESS Lab(C1)		
<b>Wed</b>	CO/ITC	EMTL	FDS/JAVA/ IDA		DSP	VLSID	EXTRA CLASSES / TRAINING
<b>Thu</b>	DSP Lab(C3)/ECAD Lab(C1)/ESS Lab(C2)				VLSID	EMTL	DSP
<b>Fri</b>	EXTRA CLASSES / TRAINING	VLSID	DSP		EMTL	CO/ITC	FDS/JAVA/ IDA
<b>Sat</b>	DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING				DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING		

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Subject	No. of Classes	Faculty	Lab Batches
VLSI Design (PC115CM)	4	G. Krishna Kishore	C1 – 20251A04C1 – 4E0, 21255A0413 – 414
EM Theory and Transmission Lines (PC115CD)	4	Dr. G. Srivalli	C2 – 20251A04E1 – 4G0, 21255A0415 – 416
Digital Signal Processing (PC115BY)	4	Ch. Ganapathy Reddy	C3 – 20251A04G1 – 4J0, 21255A0417 – 418
Computer Organization (PE115BS)	3	M. Shanthi (D305)	
Information Theory and Coding (PE115CF)	3	A. Sujatha Reddy (D302)	
Fundamentals of Data Structures (OE115KA)	3	G. Nagababu (D305)	
Java Programming (OE115KB)	3	B. Vijay Kumar (D302)	
Introduction to Data Analytics (OE115KF)	3	Dr. M. Madhavi Lata (F9 – IT Block)	
Digital Signal Processing Lab (PC11538)	3	B. Tulasi Sowjanya (C1), P. Chandrasekhar (C2), A. Sarada (C3)	
ECAD & VLSI Lab (PC11539)	3	G. Krishna Kishore (C1, C2), P. Roopa Ranjani (C3)	
Employability and Soft Skills Lab (HS11542)	3	Dr. B. Sushma (C1), V.B. Sangeetha (C2), Aswani R. Jagan (C3)	

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