

GNITS

GNITS-D / ECE / TTL / 041 / 00

YEAR WISE TIMETABLE

DEPARTMENT: ECE

Branch: ECE

Course: B. Tech

Year: 2/4 A

Semester: I

Room No.: D102

w.e.f: 10-10-22

Time / Day	1 9:00 - 10:00	2 10:00 -11:00	3 11:10-12:10	12:10 PM – 12:50 PM	4 12:50-1:50	5 1.50-2.50	6 2.50-3.50	
Mon	MA	EDC	SS	L U N C H	NT	GS		
Tue	EC Lab (A1)/BS Lab(A2)/DSD Lab(A3)				DSD	EDC	NT*	
Wed	MA	NT	SS		EC Lab (A2)/BS Lab(A3)/DSD Lab(A1)			
Thu	DSD	SS	EDC		DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING			
Fri	SS	DSD	MA		EC Lab (A3)/BS Lab(A1)/DSD Lab(A2)			
Sat	EDC*	DSD*	NT		MBC		SPDC	

(*) = Tutorial

21 Theory hours + 9 Lab hours

Note: 11:00 AM to 11:10 AM - Break

Subject	No. of Classes	Faculty	Lab Batches
Mathematical Analysis (BS113AK)	3	Dr. M. Nagasree	A1 – 21251A0401 – 422
Network Theory (ES113AQ)	4	C. Sridhar Babu	A2 – 21251A0423 – 444
Electronic Devices and Circuits (PC113AW)	4	V. Uma	A3 – 20251A0445 – 465
Signals and Systems (PC113AY)	4	Dr. Renuka Methre	
Digital System Design (PC113AT)	4	G.V.N.S.K. Sravya	
Math Bridge Course	1.5	Dr. M. Aparna	
Gender Sensitization (MC11317)	2	P. V. Asha Latha	
Electronic Circuits Lab (PC11322)	3	C. Sridhar Babu (A1, A2), V. Uma (A3)	
Basic Simulation Lab (PC11314)	3	Y. Rakesh Kumar (A1), A. Sujatha Reddy (A2), Ch. Anusha (A3)	
Digital System Design Lab (PC11320)	3	M. Shanthi (A1, A3), N. Harini (A2)	
Skill and Personality Development	1.5	P. V. Asha Latha	

Dept. Timetable Coordinator

Main Timetable Coordinator

Head of the Department

Principal

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EC Lab (C2)

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YEAR WISE TIMETABLE

DEPARTMENT: ECE

Branch: ECE

Course: B. Tech

Year: 2/4 B

Semester: I

Room No.: D104

w.e.f: 10-10-22

Time / Day	1 9:00 - 10:00	2 10:00 - 11:00	3 11:10-12:10	12:10 PM - 12:50 PM	4 12:50-1:50	5 1.50-2.50	6 2.50-3.50
Mon	EC Lab (B1)/BS Lab(B2)/DSD Lab(B3)			L U N C H	MA	NT	DSD*
Tue	SS	EDC	NT		EC Lab (B2)/BS Lab(B3)/DSD Lab(B1)		
Wed	MA	EDC	SS		DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING		
Thu	NT	SS	DSD		EC Lab (B3)/BS Lab(B1)/DSD Lab(B2)		
Fri	DSD	EDC	MA		NT	GS	
Sat	EDC*	SS*	DSD		MBC		SPDC

(*) = Tutorial

21 Theory hours + 9 Lab hours

Note: 11:00 AM to 11:10 AM - Break

Subject	No. of Classes	Faculty	Lab Batches
Mathematical Analysis (BS113AK)	3	Dr. N.V.S.L. Narasimham	B1 - 21251A0466 - 487
Network Theory (ES113AQ)	4	B. Sreekanth Reddy	B2 - 21251A0488 - 4A9
Electronic Devices and Circuits (PC113AW)	4	T. Srilatha	B3 - 20251A04B0 - 4D0
Signals and Systems (PC113AY)	4	M. Lakshmi	
Digital System Design (PC113AT)	4	G. Madhavi	
Math Bridge Course	1.5	Dr. M. Aparna	
Gender Sensitization (MC11317)	2	Aswini R Jeevan	
Electronic Circuits Lab (PC11322)	3	P. Sripadma (B1), T. Srilatha (B2, B3)	
Basic Simulation Lab (PC11314)	3	A. Sujatha Reddy (B1), Y. Rakesh Kumar (B2), Ch. Hari Prasad(B3)	
Digital System Design Lab (PC11320)	3	P. Lavanya (B1), M. Shanthi (B2), N. Harini (B3)	
Skill and Personality Development	1.5	Aswini R Jeevan	

B. S. Reddy
29/10/22
Dept. Timetable CoordinatorM. Reddy
Main Timetable Coordinator

Head of the Department

Principal

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GNITS	GNITS-D / ECE / TTL / 041 / 00
YEAR WISE TIMETABLE	DEPARTMENT: ECE

Branch: ECE Course: B. Tech Year: 2/4 C Semester: I Room No.: D206 w.e.f: 10-10-22

Time / Day	1 9:00 - 10:00	2 10:00 -11:00	3 11:10-12:10	12:10 PM – 12:50 PM	4 12:50-1:50	5 1.50-2.50	6 2.50-3.50
Mon	DSD	SS	EDC	DEPT. EVENTS / LIBRARY / EXTRA CLASSES / INTERNET/ SPORTS / TRAINING	EC Lab (C1)/BS Lab(C2)/DSD Lab(C3)		
Tue	NT	EDC	MA		SS	GS	
Wed	EC Lab (C2)/BS Lab(C3)/DSD Lab(C1)				NT	SS*	DSD
Thu	SS	MA	EDC				
Fri	EC Lab (C3)/BS Lab(C1)/DSD Lab(C2)						
Sat	NT*	MA	DSD*		MBC		SPDC

(*) = Tutorial 21 Theory hours + 9 Lab hours Note: 11:00 AM to 11:10 AM - Break

Subject	No. of Classes	Faculty	Lab Batches
Mathematical Analysis (BS113AK)	3	Dr. M. Nagasree	C1 – 21251A04D1 – 4F1
Network Theory (ES113AQ)	4	P. Sripadma	C2 – 21251A04F2 – 4H2
Electronic Devices and Circuits (PC113AW)	4	Ch. Hariprasad	C3 – 20251A04H3 – 4K4
Signals and Systems (PC113AY)	4	K. Swathi	
Digital System Design (PC113AT)	4	P. Madhuri	
Math Bridge Course	1.5	Dr. M. Aparna	
Gender Sensitization (MC11317)	2	B. Himabala	
Electronic Circuits Lab (PC11322)	3	V. Uma (C2), M. Lakshmi (C1, C3)	
Basic Simulation Lab (PC11314)	3	Ch. Hari Prasad (C3), Ch. Anusha (C1, C2)	
Digital System Design Lab (PC11320)	3	P. Madhuri (C1, C3), P. Lavanya (C2)	
Skill and Personality Development	1.5	B. Himabala	

B. Sreedh
29/10/22
Dept. Timetable Coordinator

Anudhy
Main Timetable Coordinator

[Signature]
29/10/22
Head of the Department

[Signature]
Principal

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