

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD – 500 085

EXAMINATION BRANCH

II YEAR B.TECH - I SEMESTER – R16 REGULATION SUPPLEMENTARY EXAMINATIONS APRIL/MAY-2023

TIME TABLE

TIME → FN: 10:00 AM TO 1:00 PM

BRANCH	DATE, SESSION AND DAY					
	17-04-2023 MONDAY	19-04-2023 WEDNESDAY	21-04-2023 FRIDAY	25-04-2023 TUESDAY	27-04-2023 THURSDAY	29-04-2023 SATURDAY
ELECTRONICS & COMMUNICATIONS ENGINEERING	MATHEMATICS – IV	ANALOG ELECTRONICS	NETWORK ANALYSIS	SIGNALS AND STOCHASTIC PROCESS	ELECTRICAL TECHNOLOGY	---
ELECTRICAL AND ELECTRONICS ENGINEERING (02- EEE)	MATHEMATICS – IV	NETWORK THEORY	ELECTRONIC CIRCUITS	ELECTROMAGNETIC FIELDS	ELECTRICAL MACHINES-I	--
COMPUTER SCIENCE & ENGINEERING (05- CSE)	MATHEMATICS – IV	DATA STRUCTURES THROUGH C++	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN	OBJECT ORIENTED PROGRAMMING THROUGH JAVA	---

DATE: 28-03-2023

Sd/-  
CONTROLLER OF EXAMINATIONS

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INFORMATION TECHNOLOGY (12-IT)	MATHEMATICS – IV	DATA STRUCTURES THROUGH C++	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN	OBJECT ORIENTED PROGRAMMING THROUGH JAVA	--
ELECTRONICS AND TELEMATICS ENGINEERING (17-ETM)	MATHEMATICS – IV	ANALOG ELECTRONICS	NETWORK ANALYSIS	SIGNALS AND STOCHASTIC PROCESS	ELECTRICAL TECHNOLOGY	--
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ELECTRONICS & COMMUNICATION S ENGINEERING (04- ECE)	Business Economic and Financial Analysis	-	Control Systems	Analog Communications	Pulse and Digital Circuits	Switching Theory & Logic Design
ELECTRICAL AND ELECTRONICS ENGINEERING (02- EEE)	Business Economic and Financial Analysis	---	Control Systems	Power Systems - I	Electrical Machines-II	Switching Theory & Logic Design
COMPUTER SCIENCE & ENGINEERING (05- CSE)	Business Economic and Financial Analysis	Computer Organization	Database Management Systems	Operating Systems	Formal Languages and Automata Theory	--

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INFORMATION TECHNOLOGY (12-IT)	Business Economic and Financial Analysis	Computer Organization	Database Management Systems	Operating Systems	Formal Languages and Automata Theory	---
ELECTRONICS AND TELEMATICS ENGINEERING (17-ETM)	Business Economic and Financial Analysis	--	Control Systems	Analog Communications	Pulse and Digital Circuits	Switching Theory & Logic Design

DATE: 28-03-2023

Sd/-

CONTROLLER OF EXAMINATIONS

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## KUKATPALLY , HYDERABAD – 500 085

### ***EXAMINATION BRANCH***

II YEAR B.TECH - II SEMESTER–R15,R13 REGULATION-SUPPLEMENTARY EXAMINATIONS APRIL/MAY-2023

### ***T I M E   T A B L E***

TIME→ FN: 10:00 AM TO 1:00 PM

BRANCH	DATE & DAY					
	18-04-2023 TUESDAY	20-04-2023 THURSDAY	24-04-2023 MONDAY	26-04-2023 WEDNESDAY	28-04-2023 FRIDAY	01-05-2023 MONDAY
<b>INFORMATION TECHNOLOGY (12- I T)</b>	ENVIRONMENTAL STUDIES	PRINCIPLES OF PROGRAMMING LANGUAGES	DATA COMMUNICATION	JAVA PROGRAMMING	DESIGN AND ANALYSIS OF ALGORITHMS	DATABASE MANAGEMENT SYSTEMS
<b>ELECTRICAL AND ELECTRONICS ENGINEERING (02-E E E)</b>	ELECTRONIC CIRCUITS	ELECTRICAL MACHINES-II	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	POWER SYSTEMS-I	SWITCHING THEORY AND LOGIC DESIGN	NETWORK THEORY
<b>ELECTRONICS AND TELEMATICS ENGINEERING (17-E T M)</b>	ENVIRONMENTAL STUDIES	PRINCIPLES OF ELECTRICAL ENGINEERING	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	ANALOG COMMUNICATIONS	ELECTRONIC CIRCUIT ANALYSIS	PULSE AND DIGITAL CIRCUITS
<b>ELECTRONICS AND COMMUNICATIONS ENGINEERING (04-E C E)</b>	ENVIRONMENTAL STUDIES	PRINCIPLES OF ELECTRICAL ENGINEERING	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	DIGITAL DESIGN USING VERILOG HDL	ELECTRONIC CIRCUIT ANALYSIS	PULSE AND DIGITAL CIRCUITS
<b>COMPUTER SCIENCE AND ENGINEERING (05-C S E)</b>	ENVIRONMENTAL STUDIES	DESIGN AND ANALYSIS OF ALGORITHMS	COMPUTER ORGANIZATION	JAVA PROGRAMMING	FORMAL LANGUAGES AND AUTOMATA THEORY	DATABASE MANAGEMENT SYSTEMS

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EXAMINATION BRANCH

II YEAR B.TECH - I SEMESTER – R15 ,R13 REGULATION SUPPLEMENTARY EXAMINATIONS

APRIL/MAY-2023

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COMPUTER SCIENCE & ENGINEERING	PROBABILITY & STATISTICS	ELECTRONIC DEVICES & CIRCUITS	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN	DATA STRUCTURES	BASIC ELECTRICAL ENGINEERING
ELECTRICAL AND ELECTRONICS ENGINEERING (02- EEE)	ELECTRICAL CIRCUITS	ELECTRONIC DEVICES & CIRCUITS	FLUID MECHANICS AND HYDRAULIC MACHINERY	ELECTRO MAGNETIC FIELDS	ELECTRICAL MACHINES - I	MATHEMATICS – III
INFORMATION TECHNOLOGY ( 12-IT)	PROBABILITY & STATISTICS	ELECTRONIC DEVICES & CIRCUITS	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	DIGITAL LOGIC DESIGN AND COMPUTER ORGANIZATION	DATA STRUCTURES	BASIC ELECTRICAL ENGINEERING
ELECTRONICS & COMMUNICATIONS ENGINEERING (04- ECE)	ELECTRICAL CIRCUITS	ELECTRONIC DEVICES & CIRCUITS	SIGNALS & SYSTEMS	SWITCHING THEORY AND LOGIC DESIGN	PROBABILITY THEORY & STOCHASTIC PROCESSES	MATHEMATICS – III
ELECTRONICS AND TELEMATICS ENGINEERING (17-ETM)	ELECTRICAL CIRCUITS	ELECTRONIC DEVICES & CIRCUITS	SIGNALS & SYSTEMS	SWITCHING THEORY AND LOGIC DESIGN	PROBABILITY THEORY & STOCHASTIC PROCESSES	MATHEMATICS – III