




G.NARAYANAMMA INSTITUTE OF TECHNOLOGY & SCIENCE
(For Women)


AUTONOMOUS
Shaikpet, Hyderabad - 500104
Department of ECE


Faculty Development Program on "Embedded System Design Using ARM Cortex M4
MCU's"

11th December to 14th December 2018

S.No	Name of the document	Status
1.	Proposal by the Coordinator/Convenor to Principal along with budget, and permission for the venue.	NA
2.	Invitation to the resource person	Yes
3.	Acceptance along with the bio-data	Yes
4.	Poster/brochure/Banner (Not photo)	NA
5.	Programme schedule	Yes
6.	Circular	Yes
7.	Registration/Attendance of the participants	Yes
8.	Certificates (sample)	Yes
9.	All participants filled Feedback, analysis - action	NA
10.	Resource person filled Feedback - plan of action	NA
11.	One page photos with atleast 6 with description as per the format	Yes
12.	Budget utilisation sheet	NA
13.	Press note/Newspaper publication	NA
14.	Single page report as per the format	Yes


Workshop Coordinator


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(AUTONOMOUS)
Shaikpet, Hyderabad - 500 104.


Dept HOD

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Invitation for Workshop

Date: 08/12/2018

To,
M/s EdGate Technologies Pvt. Limited,
#68, 15th cross, 1st block,
R.T.Nagar, Bangalore -560032.

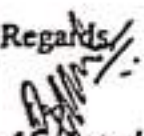
Sub: - Invitation for FDP on Embedded Systems Using ARM Cortex M4 MCU's

Dear Sir,

On Behalf of G. Narayanamma Institute of Technology and Science, Shaikpet, Hyderabad, I Dr. B.Venkateshulu, Professor and HOD Invite Resource persons Mr. Rather Sajad, Application development and R & D engineer and Mr. M. Dinakar Krish, Application Engineer from EdGate Technologies to conduct Faculty Development Program (FDP) A Four Day training workshop titled *Embedded System Design Using ARM Cortex M4 MCU's* with hands on experience on Tiva 4C123 launchpad of Texas Instruments from 11th December to 14th December 2018 in the Electronics & Communication Dept.

Thank You,

With Regards,


Dr. B. Venkateshulu
HOD, ECE Dept.,



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Acknowledgement letter for Workshop

Date: 14/12/2018

To,
M/s EdGate Technologies Pvt. Limited,
#6S, 15th cross, 1st block,
R.T.Nagar, Bangalore -560032.


Sub: - FDP on Embedded System Design Using ARM Cortex M4 MCU's

Dear Sir,

On Behalf of G. Narayanamma Institute of Technology and Science, Hyderabad I Dr. B.Venkateshulu, Professor and HOD acknowledge that EdGate Technologies has done the Four Days Faculty Development Program (FDP) on *Embedded System Design Using ARM Cortex M4 MCU's* in the Electronics & Communication Dept. The Workshop was attended by 16 Faculty on 11th December to 14th December 2018.

Thank you

With Regards,


Dr. B. Venkateshulu
HOD, ECE Dept.,

శ్రీ గాంధీ లభ్యత కేంద్రం
#6S, 15th cross, 1st block,
R.T.Nagar, Bangalore -560032

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PROFESSIONAL PROFILE

NAME: Rather Sajad

DESIGNATION: Application Development and R&D Engineer



Mr. RATHER SAJAD is employed with EdGate Technologies Pvt.Ltd(Texas Instruments University Program) as an Embedded Application Engineer. He has around 4 years of professional experience. He has completed his Post graduation from PES College of Engineering Karnataka in VLSI & Embedded systems. His working areas include Mixed Signal Processors, ARM Processors, Analog Systems Design and Wireless Design, design of embedded digital control circuitry and real-time microcontroller solutions.

He has trained over more than 3000+ students and faculties across various universities and colleges in India. Following is a glimpse of his endeavor:- B.N college Pusad, IISc Bangalore, JNTU Anantapur, IIT BHU, BMSIT Bangalore, NIT Trichy, Dr. AIT Bangalore, IIT madras, Sathyabhama University Chennai, COE Kerala, SCT Trivandrum, COEP Pune, MGM Noida, MCKV Institute of Engineering KOLKATA etc..

He can be communicated at sajad@edgate.in

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EdGate Technologies Pvt. Ltd. # 68, 15th Cross, 1st Block, R.T. Nagar, Bangalore 560032. PH:
080235351255, 23535128, Fax :080 23535128 www.edgate.in

PROFESSIONAL PROFILE



NAME: Dinakar M
 DESIGNATION: Application Engineer



Mr. Dinakar is employed with Edgate Technologies Pvt Ltd as an Embedded Application Engineer. He has 3+ years of professional experience.

He holds a Bachelor of Engineering degree from Sri Vasavi Engineering College. His interests are designing Analog and embedded technology learning about technology.

His working areas include Texas Instruments MCUs (MSP430, Tiva), DSP, ASLK prokit and IoT. He has trained numerous students and faculties in various universities and colleges across India.

Recently accomplished trainings at Kuppam Engineering College, KL University, Dr.KVSubbareddyInstituteof Engineering, St.Martins Enggineering College, Atria institute of technology, Ramaiah Institute of Technology ..etc

He can be communicated at dinakar@edgate.in

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 Shaikpet, Hyderabad - 500 104.

9/12/2021, 14:10



G. Narayanamma Institute of Technology Mat - Revised TIIC Lab

3

Harl Prasad Chindam <harichindam@gnits.ac.in>

Revised TIIC Lab
10 messages

Gurpreet Singh <singh@edgate.in>
To: harichindam@gnits.ac.in
Cc: Dinakar Krishna <dinakar@edgate.in>

Tue, Sep 19, 2017 at 12:12 PM

Dear Sir

As discussed, kindly find the proposal attached.

For any further clarification, please feel free to contact us.

With Regards,

[Gurpreet Singh]EdGate Technologies Pvt Ltd
EGL 15th Cross, 1st Block, R.T. Nagar, Bangalore-560032
Tech Support Team : 080-23535125, 23535126, Fax: 080-23535128
Sales & Marketing Team : 080 - 23331166, 23531166

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47K

Gurpreet Singh <singh@edgate.in>
To: harichindam@gnits.ac.in
Cc: Dinakar Krishna <dinakar@edgate.in>

Tue, Sep 19, 2017 at 12:29 PM

Dear Sir

Please ignore earlier mail, Use this for reference

Regards
Gurpreet Singh
(Quoted text hidden)

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47K

Harl Prasad Chindam <harichindam@gnits.ac.in>
To: renuka.devi.sm@gmail.com

Thu, Oct 12, 2017 at 12:45 PM

3

File Name

Star Office Docx
Anand Singh
G. Narayanamma

G. Narayanamma engg college_TIIC.docx
47K

Hari Prasad Chindam <harichindam@gnits.ac.in>
To: nara.kalayani@gnits.ac.in

Tue, Oct 24, 2017 at 10:01 AM

Forwarded message

From: Gurpreet Singh <singh@edgate.in>
Date: Tue, Sep 19, 2017 at 12:29 PM
Subject: Re: Revised TIIC Lab
To: harichindam@gnits.ac.in
Cc: Dinakar Krishna <dinakar@edgate.in>

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[Quoted text hidden]

G. Narayanamma engg college_TIIC.docx
47K

Mail Delivery Subsystem <mailer-daemon@googlemail.com>
To: harichindam@gnits.ac.in

Tue, Oct 24, 2017 at 10:01 AM



Address not found

Your message wasn't delivered to nara.kalayani@gnits.ac.in because the address couldn't be found. Check for typos or unnecessary spaces and try again.

LEARN MORE

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The response was:

550-5.1.1 The email account that you tried to reach does not exist. Please try 550-5.1.1 double-checking the recipient's email address for typos or 550-5.1.1 unnecessary spaces.

ATTACHED
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ATTACHED

G Narayanamma Institute of Technology Mail - Revised TIIC Lab
more at 550 5.1.1 <https://support.google.com/mail/?p=NoSuchUser> p188sor3361559ywc.554 -

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Final-Recipient: rfc822: nara.kalayani@gnits.ac.in
Action: failed
Status: 5.0.0
Diagnostic-Code: smtp; 550-5.1.1 The email account that you tried to reach does not exist. Please try
550-5.1.1 double-checking the recipient's email address for typos or
550-5.1.1 unnecessary spaces. Learn more at
550 5.1.1 <https://support.google.com/mail/?p=NoSuchUser> p188sor3361559ywc.554 - gsmtlp
Last-Attempt-Date: Mon, 23 Oct 2017 21:31:47 -0700 (PDT)

----- Forwarded message -----
From: Hari Prasad Chindam <harichindam@gnits.ac.in>
To: nara.kalayani@gnits.ac.in
Cc:
Bcc:
Date: Tue, 24 Oct 2017 10:01:45 +0530
Subject: Fwd: Revised TIIC Lab

----- Forwarded message -----
From: Gurpreet Singh <singh@edgate.in>
Date: Tue, Sep 19, 2017 at 12:29 PM
Subject: Re: Revised TIIC Lab
To: harichindam@gnits.ac.in
Cc: Dinakar Krishna <dinakar@edgate.in>

Dear Sir

Please ignore earlier mail ,Use this for reference

Regard
Gurpreet Singh

On Tue, Sep 19, 2017 at 12:12 PM, Gurpreet Singh <singh@edgate.in> wrote:

Dear Sir

As discussed ,Kindly find the proposal attached .


-

<span lang="en-in" style----- Message truncated -----

Hari Prasad Chindam <harichindam@gnits.ac.in>
To: kalyani_nara@rediffmail.com

Tue, Oct 24, 2017 at 1:09 PM

[Quoted text hidden]

 G.narayanamma engg college_TIIC.docx
47K

Hari Prasad Chindam <harichindam@gnits.ac.in>
To: singh@edgate.in
Cc: Dinakar Krishna <dinakar@edgate.in>

Tue, Nov 13, 2018 at 10:48 AM
G. Narayanamma Institute of Technology & Science (for women) Autonomous
Shaikpet, Hyderabad - 500 104.



PRINCIPAL
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Shaikpet, Hyderabad - 500 104.

Dear Sir
This is Ch Hari Prasad assistant professor ECE Dept. GNITS,
as we have semester break during 10th December to 15th December 2018 hence I would request you to conduct the
4 day faculty development workshop on IoT as per the MoU we have for setting up the TI Innovation center.
Thanking you sir
Ch Hari Prasad

2021, 14:10

Assistant professor
ECE Dept.
GNITS
9704363412
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G narayanamma institute of technology Mail - Revised TIIC Lab

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5 attachments

 Image004.jpg
3K


 Image002.jpg
3K

 Image001.jpg
15K

 Image003.jpg
2K

Alert me image005.jpg
2K

Dinakar Krishna <dinakar@edgate.in>
To: Hari Prasad Chindam <harichindam@gnits.ac.in>
Cc: ecehod@gnits.ac.in

Fri, Nov 16, 2018 at 4:21 PM

Dear Sir,

Thanks for the mail,

As per discussion with our manager, On Campus Four Days Faculty Development Program(FDP) on Embedded Systems and IoT is scheduled from 11th December 2018 to 14th December 2018.

Kindly Do the needful arrangements as soon as possible.

Regards
Dinakar.M

Hari Prasad Chindam <harichindam@gnits.ac.in>
To: Dinakar Krishna <dinakar@edgate.in>

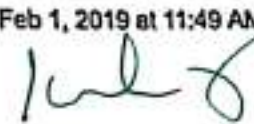
Mon, Nov 19, 2018 at 2:43 PM

Thank you so much Dinakar
(Quoted text hidden)
(Quoted text hidden)

Hari Prasad Chindam <harichindam@gnits.ac.in>
To: Chandrasekhar Paseddula <chandrasekharpaseddula@gmail.com>

Fri, Feb 1, 2019 at 11:49 AM

----- Forwarded message -----
From: Gurpreet Singh <singh@edgate.in>
Date: Tue, Sep 19, 2017 at 12:12 PM
Subject: Revised TIIC Lab
To: <harichindam@gnits.ac.in>
Cc: Dinakar Krishna <dinakar@edgate.in>

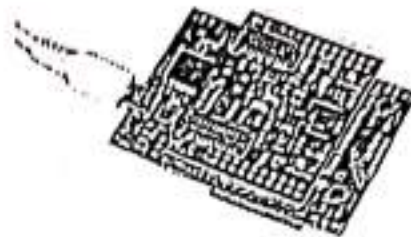

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Embedded System Four-days Workshop using Tiva C LaunchPad

This training program is designed for participants who want to sharpen their design and development skills in TI ARM Based Micro Controllers. These trainings include extensive hands-on lab emphasizing the demonstration and application of techniques and skills.



Required skills:

Basics of C programming, digital electronics, micro-controller.

DAY	9:30 – 11:00	11:15 – 00: 45	13:30- 15:00	15:15- 16:30
1	Tiva Arm Cortex M4 > Introduction > Architecture > TM4C Launchpad > TivaWare API's	Lab 1 : GPIO Configuration For Tricolored LED.	Lab 2 : GPIO Switch Configuration to Change the State of the pin.	Interrupt Processing Lab3 : Timer based Interrupt Programming
2	ADC 12 Bit operation > Resolution > ADC Block > Sample Sequencer	Lab 4 : Configuring on chip ADC to read External Potentiometer Lab 5: LM35 Temperature Sensor Interfacing	ADC Multichannel Measurements Lab 6 : Multichannel Measurements for ADC	Assignments(ADC & LED)
3	Introduction To PWM Module Lab 7 : Generation of PWM Waves using TM4c123	LAB 8 : PWM based Speed Control Of DC Motor	I2C Protocol Overview Overview of different sensors on Sensor hub BoosterPack	Lab 9: Interfacing Light Sensor using Sensor Hub BoosterPack.
4	Mini Project Design Basic Alert System design	Mini Project Design Basic Alert System design	DSP C6748 Overview Audio Processing	DSP C6748 Overview Filter design

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GNITS	GNITS /ECE-D / CIR /05/00
Circular	Department: ECE

Date 08/12/2021

Circular

Under Faculty Development Program (FDP) Four Days workshop titled "Embedded System Design Using ARM Cortex M4 MCUs" from 11th December 2018 to 14th December 2018 is being organized by ECE dept and conducted in coordination with Edgate Technologies, who are providing with hands on experience on Tiva 4C123 launchpad from Texas Instrument. Faculty who are interested and working in the domain of Embedded Systems and IoT can register and attend the training session.

Venue: TI Innovation Center, ECE Department.


Dr. B. Venkateshulu
HOD, ECE


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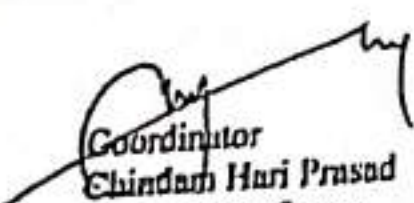
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Faculty Development Program
Embedded System Design Using ARM Cortex M4 MCU's
From 11th to 14th December 2018

Registration

S.No	Faculty Name	Designation	Dept.
1	K. Sirisha Rao	Asst. Prof.	ECE
2	N. Harini	Asst. Prof.	ECE
3	P. Roopa Ranjani	Asst. Prof.	ECE
4	G. Sravya	Asst. Prof.	ECE
5	P. Lavanya	Asst. Prof.	ECE
6	Ch. Radhika	Asst. Prof.	CSE
7	V. Uma	Assoc. Prof.	ECE
8	D. Sowjanya	Programmer	IT
9	P. Sridevi	Lab Technician	ECE
10	G. Radhika	Lab Technician	ECE
11	L. Purna Kumari	Lab Technician.	ECE
12	K. L. Varuna Kumari	Lab Technician	ECE
13	T. Ramasri	Asst. Prof	ECE
14	M. Lakshmi	Asst. Prof	ECE
15	Deepthi Amuru	Asst. Prof	ECE
16	Gundrala Madhavi	Asst. Prof.	ECE
17	T. Srilatha	Asst. Prof.	ECE
18	Chindam Hari Prasad	Asst. Prof.	ECE
19	Dr. K. Rugini	Professor	ECE
20	D. Divya	Lab Technician	ECE


Coordinator
Chindam Hari Prasad
Assistant Professor
ECE Dept.


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Dr. B. Venkateshulu
HOD, ECE Dept.,

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CERTIFICATE OF PARTICIPATION

This is to certify that L. Purna Kumari of G. Narayanamma Institute of Technology and Science has successfully participated in the Faculty Development Program for four Days from 11th to 14th December 2018 on Embedded System Design using TIVA Micro-Controller in association with Edgate Technologies Pvt Ltd.


EdGate Technologies Pvt Ltd

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This is to certify that D. Divya of G. Narayanamma Institute of Technology and Science has successfully participated in the Faculty Development Program for four Days from 11th to 14th December 2018 on Embedded System Design using TIVA Micro-Controller in association with Edgate Technologies Pvt Ltd.

[Signature]
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[Signature]
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[Signature]
Principal

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This is to certify that P. Sridevi of G. Narayanamma Institute of Technology and Science has successfully participated in the Faculty Development Program for four Days from 11th to 14th December 2018 on Embedded System Design using TIVAMicro-Controller in association with Edgate Technologies Pvt Ltd.

P. Sridevi

[Signature]
HOD-ECE

[Signature]
Principal

EdGate Technologies Pvt Ltd

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(Autonomous for women)

Hyderabad - 500 104. TEL: 050 2711111



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This is to certify that G. Radhika of G. Narayanamma Institute of Technology and Science has successfully participated in the Faculty Developed Program for four Days from 11th to 14th December 2018 on Embedded System Design using TIVA Micro-Controller in association with Edgate Technologies Pvt Ltd.

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Hyderabad - 500 104.

[Signature]
EdGate Technologies Pvt Ltd

[Signature]
HOD-ECE

[Signature]
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CERTIFICATE OF PARTICIPATION

This is to certify that *K. Srisha Rao* of *G. Narayanamma Institute of Technology and Science* has successfully participated in the *Faculty Development Program* for four Days from *11th to 14th December 2018* on *Embedded System Design* using *TIVA Micro-Controller* in association with *Edgate Technologies Pvt Ltd.*

Manoj Kumar
EdGate Technologies Pvt Ltd

HOD-ECE

Principal



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CERTIFICATE OF APPRECIATION

This is to certify that *Chindam Hari Prasad* of *G. Narayanamma Institute of Technology and Science* has successfully organized the *Faculty Development Program* for four Days from *11th to 14th December 2018* on *Embedded System Design* using *TIVA Micro-Controller* in association with *Edgate Technologies Pvt Ltd.*

Manoj Kumar
EdGate Technologies Pvt Ltd

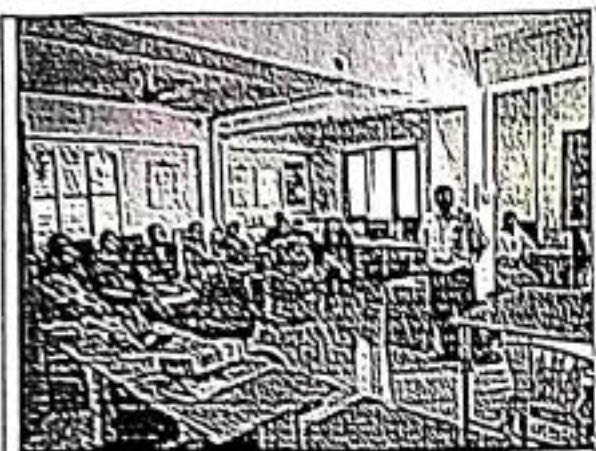
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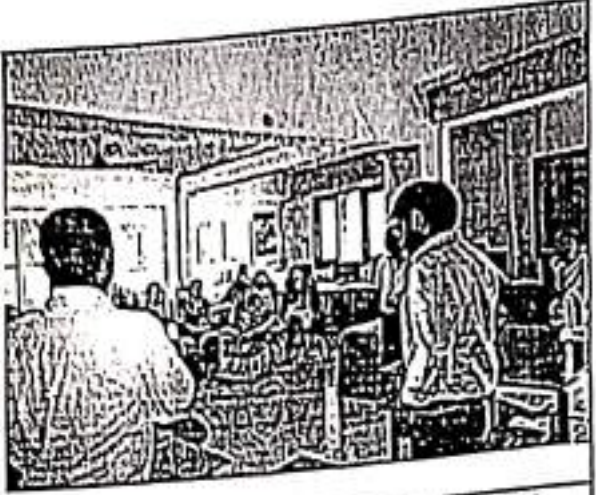
Principal



EDGATE PARTNERS



Dr. B. Venkateshulu, HoD, ECE Dept. giving Inaugural Speech on 11/12/2018.



Resource Persons Mr. Dinakar Krish and Mr. Rather Sajad.



Start of the session with description of the Tiva 4C123 Launchpad by Mr. Sajad.



Start of the session with description of the Tiva 4C123 Launchpad by Mr. Sajad.



[Handwritten Signature]
NCPAL
amma Institute of
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derabad - 500 104.

Participants working on the Launchpad.



Participants working on the Launchpad.



Dr. B. Venkateshulu, HoD, ECE Dept. appreciating resource persons from Edgate Technologies Mr. Rather Sajad and Mr. Dinakar Krish on 14/12/2018 on successful completion of the Workshop



Mr. Rather Sajad giving positive feedback on the participants and also the hospitality provided to them.

Chy
Coordinator
Chindam Hari Prasad
Assistant Professor
ECE Dept.
15/12/18

Principal
PRINCIPAL
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B. Venkateshulu
15/12/18
Dr. B. Venkateshulu
HOD, ECE Dept,



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14

Faculty Development Program
Academic year 2018-19

Report

Date: 15/12/2018


Name of the program: *Embedded System Design Using ARM Cortex M4 MCU's*
Date: 11/12/2018-14/12/2018 (4 Days)
Venue: TI Innovation Center, Ground Floor, ECE Dept.

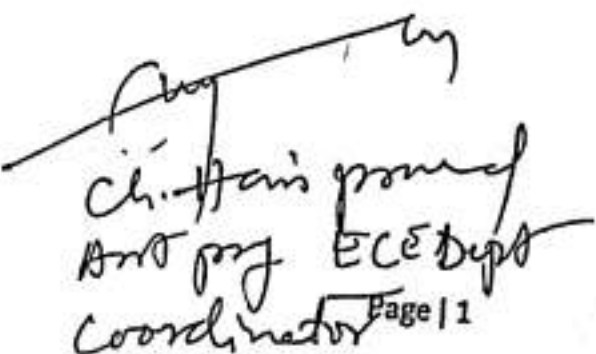
Department of ECE, G. Narayanamma Institute of Technology and Science, Hyderabad in coordination with EdGate Technologies has organized and conducted under Faculty Development Program (FDP) A Four Day training workshop titled *Embedded System Design Using ARM Cortex M4 MCU's* with hands on experience on Tiva 4C123 launchpad from Texas Instruments. This workshop was open for faculty of all departments who are interested and working in the domain of Embedded Systems & IoT and a total of 20 Faculty have registered and attended on 11th December 2018 to 14th December 2018.

The two resource persons for the training were Mr. Rather Sajad, Application development and R & D engineer and Mr. M. Dinakar Krish, Application Engineer from EdGate Technologies. They trained the participants to perform interfacing with different sensors such as accelerometer, soil sensor, temperature & humidity sensor, and also made them to execute different real time projects by teaming 3 members in a batch.

The overall feedback from the participants was positive and overwhelming, all the participants were happy with resource persons for the way they have dedicated themselves in making the participants learn the topics and do some real time projects.

Even the resource persons gave positive feedback on the participants as they have shown keen interest in learning the concepts by attending regularly.


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Ch. Hanis prasad
Asst prof ECE Dept
Coordinator Page | 1