

**G. NARAYANAMMA INSTITUTE OF TECHNOLOGY AND SCIENCE  
(For Women)**

**Department of Electrical and Electronics Engineering**

**CERTIFICATE**



This is to certify that the major project report entitled, "COMPARATIVE ANALYSIS OF DIODE CLAMPED MULTILEVEL INVERTERS" that is being submitted by **CH. Kanakalatha (18251A0296), I.Sreeshma (18251A0271), J.Navya(18251A02A2), M.Likhitha(18251A02A9), K.Prathyusha (19255A0208)** in partial fulfilment of the requirements for the award of degree of **Bachelor of Technology in Electrical and Electronics Engineering** from GNITS, autonomous is a record of bonafide work carried out by them under our guidance and supervision. The results embodied in this thesis have not been submitted to any other University or Institution for the award of any degree or diploma.

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