

DEPT. OF ELECTRONICS & COMMUNICATION ENGG
CERTIFICATE

THIS IS TO CERTIFY THAT THE MAJOR PROJECT ENTITLED

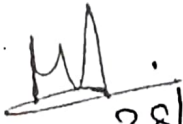
Implementation of High Speed Serial I/O using Xilinx tools in FPGA
IS THE BONAFIDE WORK OF

P. AJITA (19251A04A5)

R.NEHATA SREEYA (19251A04A6)

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE
AWARD OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS
AND COMMUNICATION ENGINEERING DURING THE YEAR 2022-2023

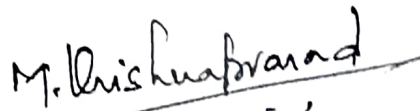
INTERNAL GUIDE



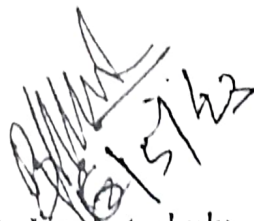
28/4/2023

Mrs. M. Shanthi
Assistant Professor, ECE

EXTERNAL GUIDE



M. Krishna Prasad
Sc-E; DLRL



Dr. B. Venkateshulu
Professor & HOD, ECE



PRINCIPAL
G. Narayanamma Institute of
Technology & Science (for women)
(AUTONOMOUS)
Shaikpet, Hyderabad - 500 101