

DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE


THIS IS TO CERTIFY THAT THE MINI PROJECT ENTITLED

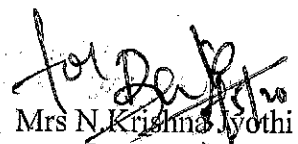
**DESIGN AND SIMULATION OF LOG PERIODIC ARRAY
ANTENNA**

IS THE BONAFAIDE WORK OF

TALARI TEJASRI	17251A04E8
GARNAPALLI SHREYA	17251A04C7
DASI SARITHA	18255A0425
KASARLA VYSHNAVI	17251A04D1

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD
OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND
COMMUNICATION ENGINEERING DURING THE YEAR 2019-2020


Dr. B. Venkateshulu
PROF & HOD, ECE


Mrs N. Krishna Jyothi
GUIDE