

DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE

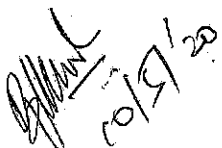
THIS IS TO CERTIFY THAT THE MAJOR PROJECT ENTITLED

**NOVEL HIGH SPEED VEDIC MULTIPLIER PROPOSAL
INCORPORATING ADDER BASED ON QUATENARY SIGNED
DIGIT NUMBER SYSTEM**

IS THE BONAFIDE WORK OF

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SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD
OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND
COMMUNICATION ENGINEERING DURING THE YEAR 2018-2019


Dr. B. Venkateshulu
PROF & HOD, ECE


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GUIDE