

**DEPT. OF ELECTRONICS & COMMUNICATION ENGG**

**CERTIFICATE**

THIS IS TO CERTIFY THAT THE MAJOR PROJECT ENTITLED

**DESIGN OF LOW-POWER HIGH PERFORMANCE 2-4 AND 4-16  
MIXED LOGIC LINE DECODERS**

IS THE BONAFIDE WORK OF

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OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND  
COMMUNICATION ENGINEERING DURING THE YEAR 2018-2019

  
Dr.B.Venkateshulu

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GUIDE