DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE

THIS IS TO CERTIFY THAT THE MAJOR PROJECT ENTITLED

DESIGN AND SIMULATION OF TRAFFIC ENGINEERING USING MPLS IN GNS3 ENVIRONMENT AND WIRESHARK

IS THE BONAFIDE WORK OF

B.Swarnalatha 16251A0470

 P.Pragna
 16251A0498

 S.Sahithi Manogna
 16251A04A8

 T Siri Snigdha
 16251A04B1

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND COMMUNICATION ENGINEERING DURING THE YEAR 2018-2019

Dr.B. Venkateshulu

EVSS Vaishnavi

PROF & HOD, ECE

GUIDE