

DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE

THIS IS TO CERTIFY THAT THE MAJOR PROJECT ENTITLED

MAC DESIGN USING VEDIC MULTIPLIER

IS THE BONAFIDE WORK OF

A.Srushna

16251A0462

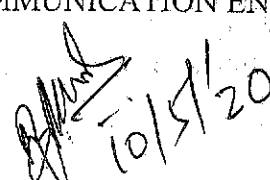
R.Pranaya

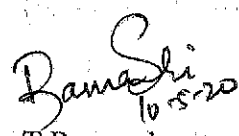
16251A04A3

E.Pravalika

17255A0419

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD
OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND
COMMUNICATION ENGINEERING DURING THE YEAR 2018-2019


Dr.B.Venkateshulu
PROF & HOD, ECE


T Ramasri
GUIDE