

**DEPT. OF ELECTRONICS & COMMUNICATION ENGG**

**CERTIFICATE**

THIS IS TO CERTIFY THAT THE MAJOR PROJECT ENTITLED

**HIGH SPEED VLSI ARCHITECTURE FOR ADVANCED  
ENCRYPTION STANDARD(AES) ALGORITHM**

IS THE BONAFIDE WORK OF

KONDABATHULA ANANYA	15251A04G4
K. MOHANA SAI LAKSHMI ABHIGNA	15251A04G3
KARRI JAYA SHREE	15251A04D3
K RACHITA	15251A04G0

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD  
OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND  
COMMUNICATION ENGINEERING DURING THE YEAR 2018-2019

*[Handwritten Signature]*  
8/10/19

Dr.B.Venkateshulu  
PROF & HOD, ECE

*[Handwritten Signature]*  
8/10/19

B.sreekanth Reddy  
GUIDE