

DEPT. OF ELECTRONICS & COMMUNICATION ENGG.

CERTIFICATE

THIS IS TO CERTIFY THAT THE MAJOR PROJECT ENTITLED

**NOVEL HIGH SPEED VEDIC MULTIPLIER PROPOSAL
INCORPORATING ADDER BASED ON QUATENERY SIGNED
DIGIT NUMBER SYSTEM**

IS THE BONAFIDE WORK OF

Rebelli Anusha

16251A04G4

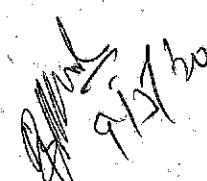
Jinne Durga

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SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD
OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND
COMMUNICATION ENGINEERING DURING THE YEAR 2019-2020


Dr. B. Venkateshulu
PROF & HOD, ECE


Dr. K. Ragini

GUIDE