

DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE

THIS IS TO CERTIFY THAT THE MAJOR PROJECT ENTITLED
**POWER-LINE INTERFERENCE DETECTION AND SUPPRESSION IN
ECG SIGNAL PROCESSING**
IS THE BONAFIDE WORK OF

Wasia Shirin

16251A04H6

Simran Viran

16251A04G7

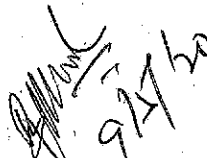
Padmavathi

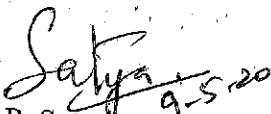
16251A04F6

Ch. Naga Harshita

16251A04D1

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD
OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND
COMMUNICATION ENGINEERING DURING THE YEAR 2019-2020


Dr. B. Venkateshulu
PROF & HOD, ECE


P. Satyanarayana Goud

GUIDE