

CERTIFICATE

THIS IS TO CERTIFY THAT THE PROJECT ENTITLED
**DESIGN OF ULTRA-LOW POWER VLSI PMOS AND NMOS FOR NANO
SCALE VLSI CIRCUITS IS THE BONAFIDE WORK OF**

G.Thrishala

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SUBMITTED IN PARTIAL FULFILLMENT OF THE REQUIREMENT FOR THE AWARD
OF DEGREE OF MASTER OF TECHNOLOGY IN DIGITAL ELECTRONICS AND
COMMUNICATION ENGINEERING DURING THE ACADEMIC YEAR 2018-2019.



HOD OF ECE DEPT
Dr. B. VENKATESHULU



INTERNAL GUIDE
Dr.K.Ragini