

DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE


THIS IS TO CERTIFY THAT THE MINI PROJECT ENTITLED


**INTELLIGENT TRAFFIC LIGHT CONTROLLER USING VERILOG
HDL**

IS THE BONAFIDE WORK OF

KEKKARLA ISHWARYA	18255A0413
GODI CHARANNI	17251A0466
BAMINI VARALAXMI	18255A0417
POGULA MEGHANA REDDY	17251A04A9

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD
OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND
COMMUNICATION ENGINEERING DURING THE YEAR 2019-2020


Dr.B.Venkateshulu
PROF & HOD, ECE


Mrs.P.Roopa Ranjani
GUIDE