

DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE


THIS IS TO CERTIFY THAT THE MINI PROJECT ENTITLED


**LOW POWER 4×4 BIT MULTIPLIER DESIGN USING DADDA
ALGORITHM**

IS THE BONAFIDE WORK OF

RASAMALLA ANUSHA	16251A04G3
GOLI YAMINISADHANA	17255A0426
SINGIREDDY RAMYA	17255A0428
ESLAVATH SHIVANI	16251A04D5

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD
OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND
COMMUNICATION ENGINEERING DURING THE YEAR 2018-2019


Dr.B.Venkateshulu
PROF & HOD, ECE


Dr.K.Ragini
GUIDE