

DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE

THIS IS TO CERTIFY THAT THE PROJECT ENTITLED

Performance Analysis of Parallel Prefix Adders

IS THE BONAFIDE WORK OF

CHERUKUMPALEM HEERA


(19251DB102)

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE
AWARD OF DEGREE OF MASTER OF TECHNOLOGY IN DIGITAL
ELECTRONICS AND COMMUNICATION ENGINEERING DURING THE YEAR
2019-2020.



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