

DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE

THIS IS TO CERTIFY THAT THE PROJECT ENTITLED


**DESIGN OF A FOUR BIT COUNTER USING
DOUBLE EDGE TRIGGERD FLIP-FLOP**


IS THE BONAFAIDE WORK OF

G.SRINIDHI

(18251D3805)

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE
AWARD OF DEGREE OF MASTER OF TECHNOLOGY IN DIGITAL
ELECTRONICS AND COMMUNICATION ENGINEERING DURING THE YEAR
2018-2019.


ECE DEPARTMENT
VENKATESHULU


INTERNAL GUIDE
Mr. B. SREEKANTH REDDY
Asst. prof


EXTERNAL EXAMINER