

DEPT.ELECTRONICS & COMMUNICATION ENGG
CERTIFICATE

THIS IS TO CERTIFY THAT THE PROJECT ENTITLED

**DESIGN AND IMPLEMENTATION OF 4X4 BIT MULTIPLIER USING
DADDA ALGORITHM**

IS THE BONAFIDE WORK OF

B.NEERAJAKSHI

(17251D3805)

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD
OF DEGREE OF MASTER OF TECHNOLOGY IN DIGITAL ELECTRONICS AND
COMMUNICATION ENGINEERING DURING THE YEAR 2018-2019.

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