

DEPARTMENT OF ELECTRONIS & COMMUNICATION
ENGINEERING

CERTIFICATE

This is to certify that the dissertation entitled "**DESIGN OF HIGH-PERFORMANCE CORE MICRO-ARCHITECTURE BASED ON RISC-V ISA**", being submitted by Ms. **Nidhi Jaiswal**, having HT No.: **21251DB101**, in partial fulfillment of the requirements for the Award of the Degree of Master of Technology in **ELECTRONICS AND COMMUNICATION ENGINEERING, with DIGITAL ELECTRONICS and COMMUNICATION ENGINEERING** Specialization, is a record of bonafide work carried out by her.

The results embodied in the Dissertation Work have not been submitted to any other University or Institute for the Award of any other Degree or Diploma.

Date:

4/9/23



Dr. K. RAGINI,
PROFESSOR,
HEAD, ECE DEPT.



Dr. B. Vasantha
External Examinee

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