

**DEPARTMENT OF ELECTRONICS & COMMUNICATION  
ENGINEERING  
CERTIFICATE**

This is to certify that the dissertation entitled “**DESIGN AND IMPLEMENTATION OF APPROXIMATE ADDER TO OPTIMIZE POWER AND AREA**”, being submitted by Ms. **K. Varnika**, having HT No.: **18251D3807**, in partial fulfillment of the requirements for the Award of the Degree of Master of Technology in **ELECTRONICS AND COMMUNICATION ENGINEERING**, with **DIGITAL ELECTRONICS AND COMMUNICATION ENGINEERING** Specialization, is a record of bonafide work carried out by her.

The results embodied in this Dissertation Work have not been submitted to any other University or Institute for the Award of any other Degree or Diploma.



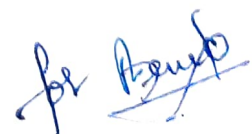
Dr. B. VENKATESHULU  
PROFESSOR,  
HEAD, ECE DEPT.

Date:

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The results embodied in this Dissertation Work have not been submitted to any other University or Institute for the Award of any other Degree or Diploma.



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Date: 09/07/2020