

**DEPT. OF ELECTRONICS & COMMUNICATION ENGG**

**CERTIFICATE**

THIS IS TO CERTIFY THAT THE PROJECT ENTITLED

**Design of High Speed Approximate Redundant Binary Multiplier**

IS THE BONAFIDE WORK OF

SUNKARA YUHA SRIDEVI

(19251DB113)

SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE AWARD OF DEGREE OF MASTER OF TECHNOLOGY IN DIGITAL ELECTRONICS AND COMMUNICATION ENGINEERING DURING THE YEAR 2019-2020.



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