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(AUTONOMOUS)

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## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING CERTIFICATE

This is to certify that the dissertation entitled "DESIGN AND IMPLEMENTATION OF LOW POWER, LOW AREA, AND HIGH-SPEED 8-BIT RADIX-8 BOOTH MULTIPLIER ON FPGA", being submitted by Ms. Sumaya Sadaf , having HTNo.21251DB102, in partial fulfillment of the requirements for the Award of the Degree of Master of Technology in ELECTRONICS AND COMMUNICATION ENGINEERING, with DIGITAL ELECTRONICS AND COMMUNICATION ENGINEERING Specialization, is a record of bonafide work carried out by her during the academic year 2022 - 23.

The results embodied in this Dissertation Work have not been submitted to any other University or Institute for the Award of any other Degree or Diploma.

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7/10/23

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