G.Narayanamma Institute of Technology and Science (For Women)
(AUTONOMOUS)

Department of ECE

Digital electronics and Logic Design Lab

About the Center

Faculty associated with DELD lab

S. No	Name of the faculty	Designation	Area of research
1	V Shankar	Assistant Professor	Low power VLSI ,mixed signal design
2	P Madhuri	Assistant Professor	Low power VLSI ,mixed signal design
3	N Harini	Assistant Professor	IOT, Signal Processing
4	P Lavanya	Assistant Professor	IOT, Signal Processing

Photos

Digital Electronics and Logic Design

Facilities

The digital electronics and logic design lab is dedicated to exploring and experimenting with digital technologies such as complex digital system design, very large-scale integration circuits, low power VLSI circuits, for many other digital circuits. The primary objective of the digital electronics and logic design lab is to implement various digital logic circuits like higher order combinational and sequential circuits with lower order circuits etc. in the area of advanced digital systems. The lab serves as a hub for research, development, and practical implementati innovative solutions to address the evolving challenges in digital system. The lab 66.6 square meters is equippe with state-of-the art infrastructure and resources to support a wide range of experiments and projects. The infrastructure enables students to design, implement, and evaluate novel digital technologies. Operating nine sessions per week, the lab is furnished with equipment worth 4,55,470. Notable resources include phytic hardware kits, multi meters, universal bread boards, CROs and function generators.

Hardware Kits

S.No	Major Components	Quantity	Unit Cost	Total Cost
1	Digital Storage Oscilloscope	4	21,665/-	86.660/-
2	Function generators	4	5,193/-	20,772/-