

**G.Narayanamma Institute of Technology and Science (For Women)**  
**(AUTONOMOUS)**  
**Department of ECE**

**Digital Electronics and Logic Design Lab**

**About the Lab:**

**Faculty associated with DELD Lab**

S. No	Name of the faculty	Designation	Area of research
1	V. Shankar	Assistant Professor	Low power VLSI ,Mixed Signal Design
2	P. Madhuri	Assistant Professor	Communications, Image Processing
3	N. Harini	Assistant Professor	IOT, Embedded Systems
4	P.Lavanya	Assistant Professor	Embedded Systems ,Communications

**Digital Electronics and Logic Design Lab Photo:**



## Digital Electronics and Logic Design Lab Facilities:

The Digital Electronics and Logic Design lab area is 66.6 square meters, equipped with good infrastructure and resources to support a wide range of experiments and hobby projects. The lab is operating with nine sessions per week, and furnished with equipment worth of Rs.3,56,696. The lab is dedicated to explore and experiment various digital circuits such as adders, subtractors, multiplexers, code converters, flip-flops, registers and counters ICs. The medium scale integrated circuits will be implementing using small scale integrated circuits. The primary objective of the digital electronics and logic design lab is to implement various digital circuits like combinational and sequential circuits. The lab serves as a hub for research, development, and practical implementation of innovative solutions to address the evolving challenges in digital circuits.

### List of Equipment:

S.No	Name of The Equipment	Quantity	Cost in Rs
1	Experimental kits	30	1,11,666/-
2	General Purpose IC Trainer kits	15	1,26,403/-
3	Dual Trace Oscilloscope	4	86,659/-
4	Function generators	4	20,768/-
5	Digital Multimeters	8	11,200/-

### Hobby projects carried out by Students in DELD Lab:

Batch No.	Roll No.		Title of the Hobby Projects	Name of the Supervisor
1	21251A04E6	M.Varsha Reddy	Seat belt warning system	P. Madhuri
2	21251A04D5	C.Harini	Water Level control using AND and NOT gate	P. Madhuri
3	18251A04G0	G.Samanvitha	2 to 4 Decoder using NAND and NOR gates	T.Srilatha
4	17251A0474	Neha Cemerla	BCD to Seven Segment Display.	N. Harini
5	17251A0409	K Likitha	Implementation of full adder using 3-to- 8 decoder	E.V.S.S Vyshnavi
6	16251A0443	R Madhulatha	Mosquito repellent circuit using IC 555 timer	N. Harini
	16251A0445	S Navya		
	17255A0409	P Triveni		

